

L Number	Hits	Search Text	DB	Time stamp
-	1	("5157620").PN.	USPAT	2004/06/15 08:07
-	1	("0854429").PN.	USPAT; EPO	2004/06/15 08:08
-	354	rayner.in.	USPAT; US-PGPUB; EPO	2004/06/15 08:08
-	2	rayner.in. and (digital adj system adj simulation)	USPAT; US-PGPUB; EPO	2004/06/15 12:14
-	590	703/13.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/15 12:14
-	1	703/13.ccls. and (VHDL adj simulation adj model or VHDL+ adj simulation adj model)	USPAT; US-PGPUB; EPO; JPO	2004/06/15 12:17
-	79	703/13.ccls. and (VHDL or VHDL+)	USPAT; US-PGPUB; EPO; JPO	2004/06/15 12:17
-	34	(703/13.ccls. and (VHDL or VHDL+)) and (simulation adj model)	USPAT; US-PGPUB; EPO; JPO	2004/06/15 12:17
-	4	((703/13.ccls. and (VHDL or VHDL+)) and (simulation adj model)) and kernel	USPAT; US-PGPUB; EPO; JPO	2004/06/15 12:20
-	12	(703/13.ccls. and (VHDL or VHDL+)) and (process and signal and queue)	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:20
-	590	703/13.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:20
-	0	703/13.ccls. and (VHDL adj interface)	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:21
-	0	715/54,55.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:21
-	0	715/54.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:23
-	245	710/54,55.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:23
-	5	710/54,55.ccls. and VHDL	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:24
-	3	(710/54,55.ccls. and VHDL) and queue	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:28
-	2719	VHDL or VHDL+	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:29
-	1821	(VHDL or VHDL+) and interface	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:29
-	111	((VHDL or VHDL+) and interface) and kernel	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:29
-	38	((((VHDL or VHDL+) and interface) and kernel) and queue) and signal	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:30
-	38	((((VHDL or VHDL+) and interface) and kernel) and queue) and signal) and process	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:58
-	254	(message adj composition) or (message adj decomposition)	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:58
-	2	((message adj composition) or (message adj decomposition)) and (VHDL or VHDL+)	USPAT; US-PGPUB; EPO; JPO	2004/06/16 08:59
-	39	((VHDL or VHDL+) and interface) and kernel) and queue	USPAT; US-PGPUB; EPO; JPO	2004/06/16 13:18

-	2719	VHDL or VHDL+	USPAT; US-PGPUB; EPO; JPO	2004/06/16 10:51
-	0	(VHDL or VHDL+) and (shedul\$3 adj queue)	USPAT; US-PGPUB; EPO; JPO	2004/06/16 10:52
-	27	((((VHDL or VHDL+) and interface) and kernel) and queue) and schedul\$3	USPAT; US-PGPUB; EPO; JPO	2004/06/16 10:53
-	1636	VHDL or VHDL+	USPAT	2004/06/16 13:19
-	155	(VHDL or VHDL+) and (simulation adj model)	USPAT	2004/06/16 13:19
-	1	((VHDL or VHDL+) and (simulation adj model)) and (event adj type)	USPAT	2004/06/16 13:19